

Form PTO-1449	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DKT. NO. 500.39826CX1	SERIAL NO. Unassigned
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)		APPLICANT Seiichiro KANNO <i>et al.</i>	
		FILING DATE 7 October 2003	GROUP Unassigned

U.S. PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date
<i>W</i>	AA 5,808,415	09/15/1998	Hopkins			
	AB 6,061,006	05/09/2000	Hopkins			
	AC 5,939,886	08/17/1999	Turner <i>et al.</i>			
	AD 6,091,257	07/18/2000	Verkuil <i>et al.</i>			
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	AJ					
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	AL					

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Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation/Abstract	
						Yes	No
<i>W</i>	AM 07-135180	05/23/1995	JP				
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	AR						
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<i>W</i>	AU	Smith, CIRCUITS, DEVICES, AND SYSTEMS-A First Course in Electrical Engineering, 3 RD Ed., John Wiley & Sons, Inc., 1973.
	AV	
	AW	
	AX	
	AY	
	AZ	

Examiner

KAPLA MOORE

Date Considered

10/3/05

Form PTO-1449
EquivalentU.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICE

ATTY. DKT. NO.

500.49826CX1

SERIAL NO.

10/679,342

INFORMATION DISCLOSURE STATEMENT
BY APPLICANT

(Use several sheets if necessary)

APPLICANT

Seiichiro KANNO *et al.*

FILING DATE

7 October 2003

GROUP

1763

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Ex. Initial	Doc. No.	Date	Name	Class	Subclass	Filing Date
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						Yes	No
<i>ku</i> AM	06-287760	10/11/1995	JP			Abstract	
AN	09-129594	05/16/1997	JP			Abstract	
AO	11-167997	06/22/1999	JP			Abstract	
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KANNO, et al.FILING DATE
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FOREIGN PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation /Abstract	
						Yes	No
AM	9-129594	5-16-97	Japan			X	
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Examiner

LARA MOORE

Date Considered

10/5/08

